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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of: Alon et al

Serial No.: 10/091,934

Group Art Unit: 2825

Filed: March 06, 2002

Examiner: Naum B Levin

Confirmation No.: 7058

Date: May 1, 2006

Title: AN INTERCONNECT-AWARE METHODOLOGY FOR INTEGRATED
CIRCUIT DESIGN

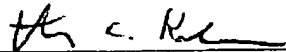
Honorable Commissioner for Patents

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I hereby certify that this paper (6 pages, Amendment and Remarks and Petition for Extension of Time – one month) is being facsimile transmitted under Rule 37 CFR 1.6(d) to the U.S. Patent and Trademark Office to 571-273-8300 on May 1, 2006.



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Amendment

Sir:

This amendment is filed in response to the Office Action dated 29 December 2005 issued by the USPTO in connection with the above-identified patent application. A response to the 29 December 2005 Office Action was due 29 March 2006. Applicants are concurrently filing a Petition for a one-month extension of time, including the required fee. Therefore a response is due Saturday 29 May 2006, allowing for submission on the first working day thereafter, 1 May 2006. Accordingly, this Amendment is being timely filed.

Serial No. 10/091,934

1